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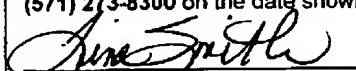
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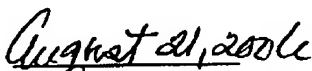
**AUG 21 2006****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of  
**Pendharker et al.**

Docket Number: **TI-33654**Serial No.: **10/678,028**Art Unit: **2814**Filed: **09/29/2003**Examiner: **T. Doan**For: **AN INTEGRATED CIRCUIT DEVICE WITH A VERTICAL JFET****CERTIFICATION OF FACSIMILE TRANSMISSION**

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<b>NAME OF INVENTOR(S):</b> <b>Pendharker et al.</b>	
<b>TITLE OF INVENTION:</b> <b>A Low Noise Vertical Variable Gate ...</b>	
<b>TI FILE NO.:</b> <b>TI-33654</b>	<b>DEPOSIT ACCT. NO.:</b> <b>20-0668</b>
<b>RECEIPT DATE &amp; SERIAL NO.:</b> <b>Serial No.: 10/678,028</b> <b>Conf. No: 9934</b>	
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Texas Instruments Incorporated  
PO Box 655474, M/S 3999  
Dallas, TX 75265